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# (54) SYSTEMS AND METHODS FOR IMPROVING ACCURACY OF TEST MEASUREMENTS INVOLVING AGGRESSOR TRACKS WRITTEN TO DISKS OF HARD DISK DRIVES

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# (58) Field of Classification Search

None

See application file for complete search history.

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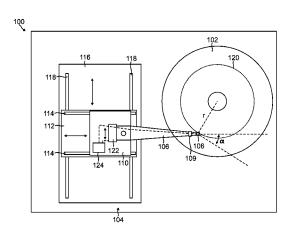
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### (57) ABSTRACT

Systems and methods for improving the accuracy of test measurements involving aggressor tracks written to the disks are provided. One such method involves erasing a circumferential band of a disk, writing a central track on the circumferential band, measuring and storing a first track profile of the central track, writing an aggressor track on each side of the central track at a preselected aggressor track offset from the central track, measuring and storing a track profile of the aggressor tracks at the preselected aggressor track offset, measuring and storing a second track profile of the central track, performing the prior actions exactly n times where n is greater than or equal to 1, determining an estimated distance between the aggressor tracks based on the respective track profiles, determining a selected measurement using the estimated distance between the aggressor tracks, and the first and second track profiles of the central track.

# 32 Claims, 7 Drawing Sheets



# US 9,093,122 B1 Page 2

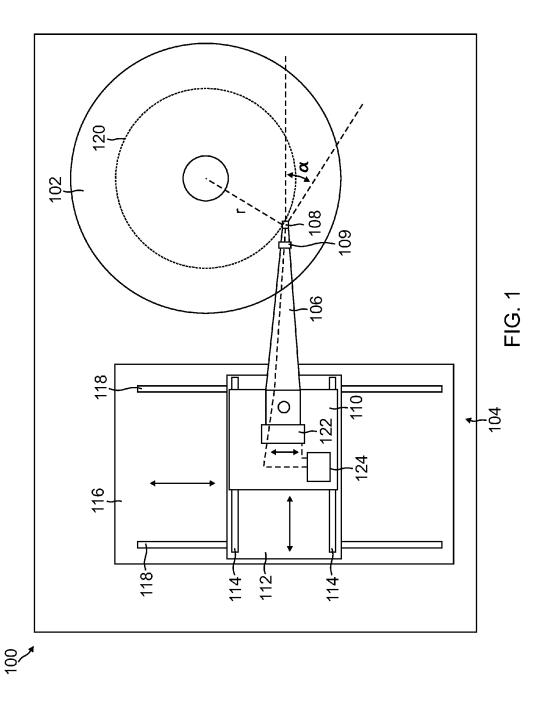
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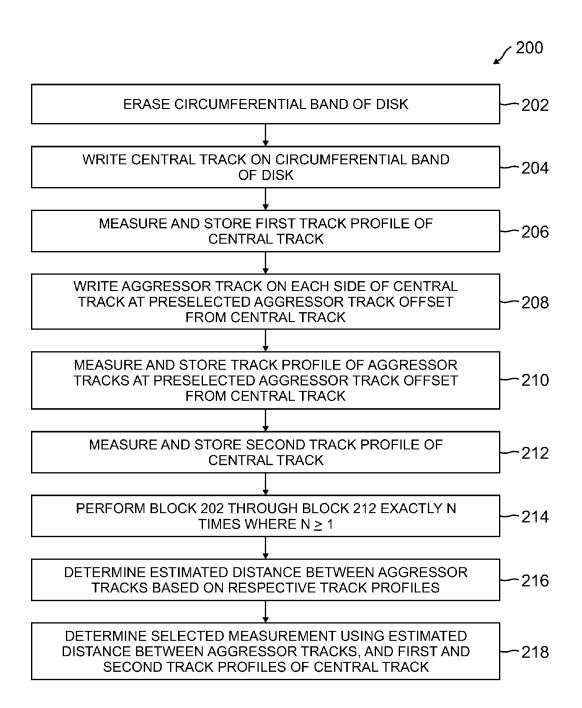
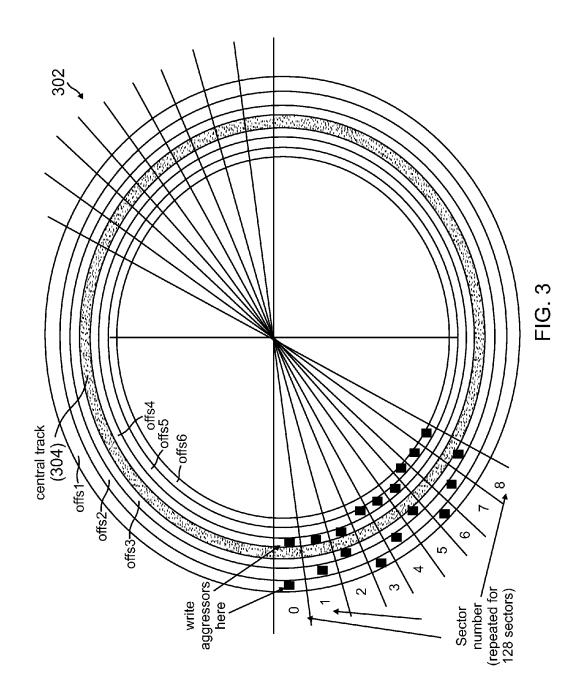


FIG. 2



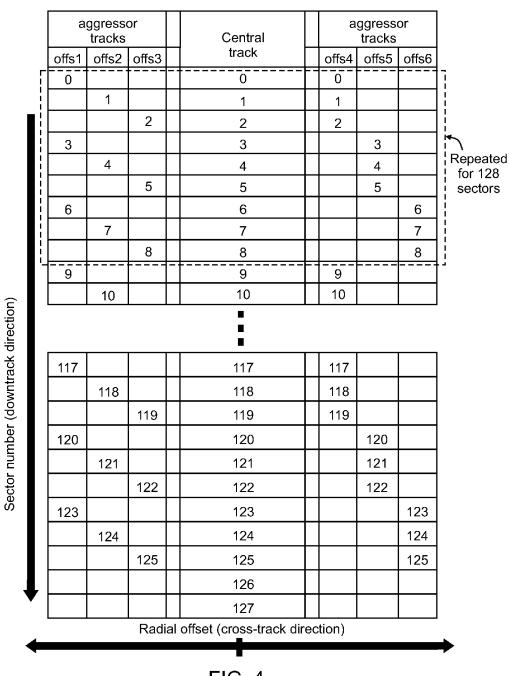
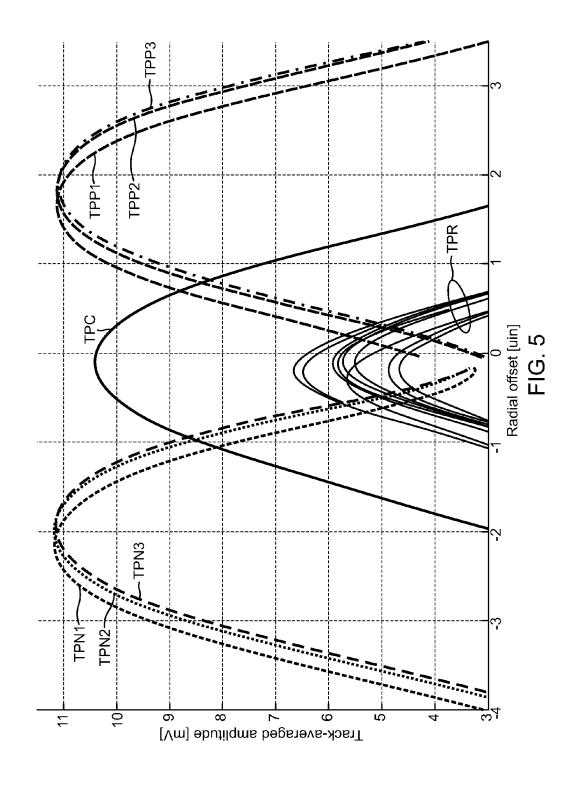
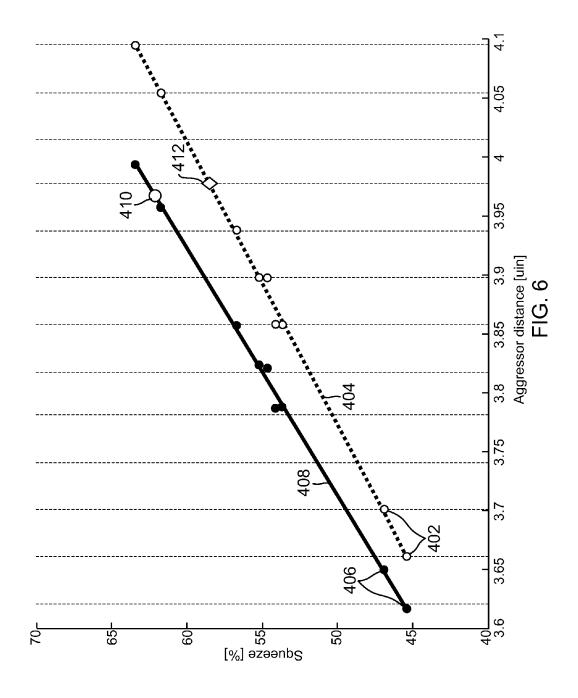
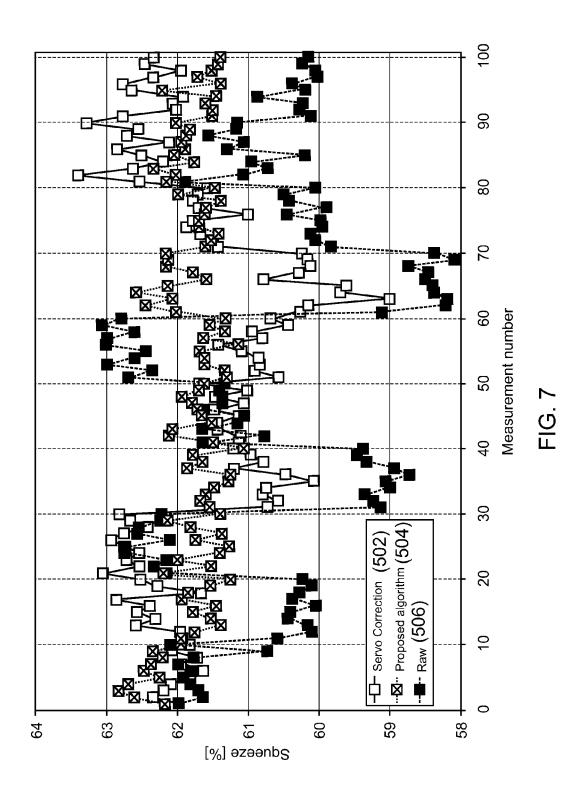


FIG. 4







# SYSTEMS AND METHODS FOR IMPROVING ACCURACY OF TEST MEASUREMENTS INVOLVING AGGRESSOR TRACKS WRITTEN TO DISKS OF HARD DISK DRIVES

# CROSS-REFERENCE TO RELATED APPLICATION(S)

This application claims priority to and the benefit of U.S. Provisional Application No. 61/809,231, filed on Apr. 5, 2013, entitled, "METHODS FOR IMPROVING THE ACCURACY OF SPINSTAND MEASUREMENTS INVOLVING AGGRESSOR WRITES", the entire content of which is incorporated herein by reference.

### **FIELD**

The present invention relates generally to measurements of disks of a hard disk drive (HDD), and more specifically to systems and methods for improving the accuracy of test measurements involving aggressor tracks written to the disks.

## BACKGROUND

Spin stand testing systems (spinstands) are used for multiple purposes with respect to HDD disks, including disk characterization, testing, and development. In order to perform such operations, the systems need to provide accurate and precise positioning of read-write heads at various track offsets for the purposes of reading and writing to disks. Additionally, though most measurements on a spinstand write at offset 0, certain measurements can also involve accurate and precise positioning of the head at off-track positions for writing. Accordingly, positioning of a head for spinstands is facilitated by using a servo in combination with servo marks (also known as "servo patterns") in a closed-loop system. The servo marks are written in small sectors on each disk and are used to accurately position the head at different read or write offsets.

Unfortunately, the quality of the servo patterns can greatly affect the repeatability of some essential measured results 40 from spinstands. In general, the head positioning of a conventional servo in a spinstand (e.g., from Guzik Technical Enterprises of Mountain View, Calif.) varies over a range of read or write offsets and is not repeatable from servo-to-servo write. Furthermore, though approaches to improve head positioning 45 accuracy have been developed (e.g., Guzik Servo Improvement Package), these solutions can suffer from drawbacks such as mechanical limitations that limit accuracy and a significant increase in operation time of the spinstand.

Many recording measurements involving a spinstand use a sequence like (1) write a central track, (2) write additional "aggressor" tracks at off track locations, and (3) assess how much the central track has changed. A servo pattern written on the disk before a sequence of tests is used to position the head at the desired radial location. However, this servo pattern is not perfect. There are variations from one writing of the servo to the next. In addition, the servo system may have a minimum step size which limits where the head can be positioned. Both of these problems degrade the accuracy for writing the "aggressor" tracks at precisely the desired location.

## BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a top schematic view of a spin stand testing system configured to obtain accurate test measurements involving 65 aggressor tracks written on a disk in accordance with one embodiment of the invention.

2

FIG. 2 is a flow chart of a process for obtaining accurate test measurements involving aggressor tracks written on a disk in accordance with one embodiment of the invention.

FIG. 3 is a top schematic view of a disk having a central test track written initially and aggressor tracks written adjacent to the central track later, where the disk has been divided into sectors to increase process efficiency, in accordance with one embodiment of the invention.

FIG. 4 is a table illustrating disk sectors numbers and radial offsets for a central test track and various aggressor tracks written at preselected offset positions relative to the central track in accordance with one embodiment of the invention.

FIG. 5 is a graph illustrating an example of measured track profiles including an original central test track, residual
 tracks, and aggressor tracks in accordance with one embodiment of the invention.

FIG. 6 is a graph illustrating a squeeze test measurement as a function of the distance between aggressor tracks in accordance with one embodiment of the invention.

FIG. 7 is a graph illustrating squeeze test measurement data acquired on a commercial spinstand in a series of repeated measurements with ten servo writes in accordance with one embodiment of the invention.

# DETAILED DESCRIPTION

Embodiments of systems and methods described herein can substantially improve the accuracy of measurements that involve aggressor track writes in two ways. First, the systems and methods described herein can measure the actual location where the aggressor tracks were written. Present measurements just request the desired location and assume it has been achieved accurately. Using the techniques disclosed here one can determine precisely where these tracks were written, not just where they were intended to be written. Second, in the systems and methods described herein, the aggressors tracks can be written at several off-track locations, both closer and further away than desired from the central track. This gives data over a range of aggressor track locations which can be interpolated to the desired aggressor track location.

In several instances, the systems and methods are described herein in relation to a squeeze measurement. However, the same concepts can be applied to other measurements that involve off-track aggressor track writing such as magnetic track width measurements (MTW), signal to noise measurements (e.g., wsSNRfinal), error margin measurements (e.g., EMfinal), shingled error margin measurements (ShEM), and other suitable measurements.

FIG. 1 is a top schematic view of a spin stand testing system 100 configured to obtain accurate test measurements involving aggressor tracks written on a disk in accordance with one embodiment of the invention. The spin stand testing system 100 includes a disk 102 and a positioning device 104. The disk 102 is representative of a magnetic recording disk that would be used in a hard disk drive and is configured to rotate around an axis at a variable rotation rate. The positioning device 104 secures a head gimbal assembly (HGA) 106 that includes a head 108. The HGA 106 also includes a microactuator 109 that is configured to laterally translate a read sensor of the head 108, for instance, by translating the entire head 108 as shown, or by translating just a transducer of the head 108 that includes the read sensor.

The positioning device 104 is configured to position the head 108 to a desired position on the disk 102, for example, with a combination of coarse and fine positioners for translating the HGA 106. As shown in FIG. 1, the positioning device 104 includes both coarse and fine positioners. The

coarse positioning of the head 108 is performed by two platforms on orthogonal rail systems, while the fine positioning is performed by a secondary mover 122.

Turning first to the coarse positioners, the HGA 106 is secured to a base 110 on a first platform 112. The first platform 112 includes a set of rails 114 upon which the base 110 can move back and forth in a first direction relative to the first platform 112. A motor (not shown) is one example of a mechanism for driving the base 110 relative to the first platform 112. Similarly, the positioning device 104 also has a second platform 116 including a set of rails 118. In this embodiment the first platform 112 is configured to move upon the rails 118 relative to the second platform 116 in a second direction.

By moving the base 110 relative to the first platform 112, and by moving the first platform 112 relative to the second platform 116, the head 108 can be positioned at a desired disk radius, r, and skew angle, a (an angle formed between a longitudinal axis of the HGA 106 and a tangent to a radial line through the head 108). It will be appreciated, however, that various types of coarse positioning means, and not just that employed by the positioning device 104 to move the base 110 relative to the disk 102, may be used in accordance with embodiments of the invention.

In addition to the orthogonal rail system for coarse positioning, the positioning device 104 also includes the secondary mover 122. The secondary mover 122 provides a finer positioning capability in a lateral direction than is provided by the orthogonal rail system. The secondary mover 122 provides fine positioning, for example, through the use of an actuator based on a piezoelectric material. Preferably, the secondary mover 122 is able to move the head 108 in steps that are on the order of a micro-inch or less.

The positioning device 104 further includes a processor 124 that may be electrically coupled to the head 108, the 40 microactuator 109, the secondary mover 122, and the coarse and fine positioners. The processor 124 can include a memory configured to store information such as instructions to be executed on the processor 124 or other information. In some embodiments, the memory is integrated with processor 124. In other embodiments, the memory is not integrated with processor 124. The processor 124 is configured to execute instructions for operating the spin stand testing system 100.

In this context, the processor 124 refers to any machine or selection of logic that is capable of executing a sequence of instructions and should be taken to include, but not limited to, general purpose microprocessors, special purpose microprocessors, central processing units (CPUs), digital signal processors (DSPs), application specific integrated circuits (ASICs), signal processors, microcontrollers, and other suitable circuitry. Further, it should be appreciated that the term processor, microprocessor, circuitry, controller, and other such terms, refer to any type of logic or circuitry capable of executing logic, commands, instructions, software, firmware, functionality, or other such information.

FIG. 2 is a flow chart of a process 200 for obtaining accurate test measurements involving aggressor tracks written on a disk in accordance with one embodiment of the invention. In particular embodiments, the process 200 can be executed on  $^{65}$  the spin stand test system 100 of FIG. 1, and more specifically, on the processor 124 of FIG. 1.

4

FIG. 2-Block 202:

The process first erases a circumferential band of a disk in block **202**. In several embodiments, the process can erase a range of write offsets to be used in the process.

FIG. 2—Block 204:

The process then writes a central track on the circumferential band of the disk in block 204.

FIG. 2—Block 206:

The process then measures and stores a first track profile of the central track in block **206**. In several embodiments, the process measures the amplitude as a function of radial offset at the central track to generate the first track profile of the central track.

FIG. 2—Block 208:

The process then writes an aggressor track on each side of the central track at a preselected aggressor track offset from the central track in block 208. In some embodiments, the process generates a list of off-track positions where the aggressor tracks will be written on each side of the central track. In one such embodiment for a squeeze measurement, two additional off-track locations are used on each side in addition to the location where the first aggressor track is intended to be written. For example, if squeeze is desired for aggressor tracks located at plus and/or minus a preselected desired offset (e.g., aggressor offset0) from the central track location, the three values on one side will be offset1, offset2, and offset3, which are equal to <aggressor\_offset0> plus or minus Npos multiplied by (piezo actuator step size) where Npos is an integer and <aggressor\_offset0> is the discretized version of aggressor\_offset0 in units of the piezo actuator step size. Similarly, the off-track locations for aggressors on the other side will be offset4, offset5, and offset6, which are equal to negative <aggressor\_offset0> plus or minus Nneg multiplied by (piezo actuator step size), where Nneg is an integer.

In some embodiments, the process writes the aggressor tracks only to preselected sectors of the disk in block 208. In such case, the process can generate a list of preselected sectors for aggressor tracks and associate the sectors with the off-track locations for aggressor tracks. Examples of sector layouts that can be used are shown in FIG. 3 and FIG. 4.

FIG. 3 is a top schematic view of a disk 302 having a central test track 304 written initially and aggressor tracks (offs1, offs2, offs3, offs4, offs5, offs6) written adjacent to the central track 304 in select sectors later, where the disk has been divided into sectors (only 0 to 8 shown) to increase process efficiency, in accordance with one embodiment of the invention. In several embodiments, the disk 302 is divided up into about 128 sectors.

FIG. 4 is a table illustrating disk sectors numbers (0 to 127) and radial offsets for a central test track and various aggressor tracks written at preselected offset positions (offs1, offs2, offs3, offs4, offs5, offs6) relative to the central track in accordance with one embodiment of the invention. In several embodiments, the sectors, radial offsets and central track of FIG. 4 corresponds to those of FIG. 3. In FIG. 4, the aggressor track at offset "offs1" is written in sectors 0, 3, 6, 9, etc. Writing the aggressors at three radial locations on each side of the center track creates 3 by 3 squeeze configurations (e.g., nine possible combinations of aggressor track locations). One goal of the sector allocation is to maintain the independence of the nine squeeze measurements without making the measurement impractical from a test time point of view. In FIG. 4, the aggressor tracks are written at three off-track locations on the left side (offs1, offs2, offs3) and at three off-track locations on the right side (offs4, offs5, offs6). This sector allocation scheme (e.g., the one from sectors 0 to 8) is repeated in the downtrack direction until all the sectors in a revolution are , ,

filled. When reading the central track after the aggressor writes, certain sectors are associated with certain off-track aggressor write locations.

5

### FIG. 2—Block 210:

Returning now to FIG. 2, the process then measures and 5 stores a track profile of the aggressor tracks at the preselected aggressor track offset from the central track in block 210. In some embodiments, the process writes the aggressor tracks only to preselected sectors of the disk in block 208. In such case, the process measures and stores the track profile of the 10 aggressor tracks at the preselected aggressor track offsets from the central track in the preselected sectors of the disk in block 210. In one such case, see for example the sectors of FIG. 4, the process also selects the preselected aggressor track offsets and the preselected sectors such that for a pre- 15 selected group of the preselected sectors, each combination of the preselected aggressor track offsets for a particular sector are unique. In one embodiment, the preselected group of the preselected sectors is about 9 sectors (see for example sectors 0 to 8 in FIG. 4). In several embodiments, the sector combinations present in the preselected group of 9 sectors can be repeated for every 9 sectors up to sector 127 of the disk.

In several embodiments, the process measures and stores the track profile of the aggressor tracks at the preselected aggressor track offset from the central track in block 210 by 25 performing a servo calibration procedure and thereby generating a read offset correction table, and applying the read offset correction table to the track profiles of the aggressor tracks thereby generating corrected track profiles of the aggressor tracks, where the process then determines the esti-30 mated distance between the aggressor tracks based on the respective track profiles based on the respective corrected track profiles in block 216, to be discussed in more detail below. In one such embodiment, the process performs the servo calibration procedure by writing a series of tracks over 35 a range of read offsets to be calibrated, measuring a set of raw track profiles from the series of tracks, sampling the set of raw track profiles at a series of signal amplitude levels, constructing a reference track profile from the set of sampled track profiles, calculating a set of read offset deltas from each 40 sampled track profile, and merging the sets of read offset deltas into a set of average read offset deltas stored in the read offset correction table. More details on the server calibration procedure can be found in U.S. patent application Ser. No. 13/115,307, entitled, "SYSTEM AND METHOD FOR 45 IMPROVING HEAD POSITIONING", the entire content of which is incorporated herein by reference. In some embodiments, the process performs the servo calibration procedure prior to erasing the circumference band of the disk in block 202.

# FIG. 2—Block 212:

The process then measures and stores a second track profile of the central track in block **212**. In several embodiments, the process generates a list of sectors for reading back the center track after the aggressor writes. For example and in reference to table of FIG. **4**, to assess the damage created by the aggressor at offs1 (sectors  $0, 3, 6, 9, \ldots$ ) and the corresponding damage at offs4 (sectors  $0, 1, 2, 9, 10, 11, \ldots$ ), the algorithm can use the common sectors  $(0, 9, 18, \ldots)$ . Similarly, to assess the damage created by the aggressor at offs1 (sectors  $0, 3, 6, 60, \ldots$ ) and the corresponding damage at offs5 (sectors  $3, 4, 5, 12, 13, 14, \ldots$ ), the algorithm can use the common sectors  $(3, 12, 21, \ldots)$ .

# FIG. 2—Block 214:

The process then performs blocks **202** through **212** exactly 65 n times where n is greater than or equal to 1 in block **214**. In one embodiment, n is greater than or equal to 2. In another

6

embodiment such as that illustrated in FIG. 4, n is equal to or greater than 3 such that at least 3 iterations of blocks 202 through 212 are performed. In such case, the process can write aggressor tracks to the preselected sectors such that the process writes 1 sector for every 3 consecutive sectors on a first side of the central track, writes 3 consecutive sectors for every 9 consecutive sectors on a second side of the central track, and performs these two sector writes exactly 3 times at 3 different preselected aggressor track offsets. In one such case, the process can repeat these actions until all sectors around the disk have been written with the aggressor tracks. In other embodiments, other sector selection techniques can be used for choosing the sectors to write the aggressor tracks.

FIG. 2—Block 216:

The process then determines an estimated distance between the aggressor tracks based on the respective track profiles in block 216. In one embodiment, the process is applied for a squeeze measurement. In such case and in order to obtain the dependence of squeeze on the aggressor distance, an accurate assessment of the distance between aggressor tracks is needed. This goal can be achieved in two steps. First, the aggressor tracks are measured by means of track profiles in particular sectors established in the discussion of block 208 above. In one aspect, three track profiles (TPN1, TPN2, TPN3) are obtained at negative offsets, representing the aggressor tracks written at offs1, offs2, offs3, respectively, and similarly three track profiles (TPP1, TPP2, TPP3) are obtained at the positive offsets, representing the aggressor tracks written at offs4, offs5, offs6, respectively. An example of such measured track profiles is shown in FIG. 5.

FIG. 5 is a graph illustrating an example of measured track profiles including an original central test track (TPC), residual tracks (TPR), and aggressor tracks (TPN1, TPN2, TPN3, TPP1, TPP2, TPP3) in accordance with one embodiment of the invention. The vertical axis shows the trackaveraged amplitude in millivolts (mV), and the horizontal axis shows the radial offset in micro-inches (uin). On each side of the center track the aggressor tracks are written in different sectors and at different off-track locations. The positioning of the aggressor tracks at multiple locations results in residual center profiles (TPR) with various peak amplitudes and central locations. One feature related to aspects of this novel process involves the observation that conventional techniques rely on either raw or numerically corrected offtrack positions reported by the piezo actuator, whereas the present techniques can measure the actual location where the aggressors tracks have been written, thus reducing both the repeatable (systematic) and non-repeatable (non-systematic) radial positioning errors.

The process can estimate the distance between the aggressors located on opposite sides with respect to the central track by using the measured track profiles and the servo correction function for read offsets (determined in block 210).

The servo correction function for read offsets is used to correct the track profiles of the aggressor tracks such that TPN1, TPN2, TPN3 are converted to corrTPN1, corrTPN2, corrTPN3. The corrected track profiles from the negative offset side, corrTPN1,2,3, are paired with those from the positive side, corrTPP1,2,3, to calculate the distance between them where the aggressor distance (i,j) or "aggr\_disti,j" is equal to the distance between (corrTPNi, corrTPPj). Several techniques can be used to calculate the distance between two track profiles. In one embodiment, for example, the process can use the algorithm implemented in the U.S. patent application Ser. No. 13/115,307, incorporated by reference above, where the two aggressor tracks are re-sampled at equal values of amplitude and numerically offset-shifted to find the opti-

mal overlap of the track profiles in a least-squares sense. In other embodiments, other suitable techniques can be used.

With the squeeze measurement values determined in block 212 and the aggressor distances estimated in block 216, the process can provide the dependence of a squeeze measurement on the aggressor distance, or Squeeze(aggr\_disti,j). In one aspect, this dependence is linear for the range of parameters that are typically used in head/media testing. The slope and intercept of the Squeeze(aggr\_disti,j) can be used to calculate squeeze at the desired aggressor\_offset(), thus overcoming issues caused by the finite radial step size of the piezo actuator described above. This is another feature of the present process that allows the user to overcome the hardware limitation associated with the finite step size of the radial positioning system. An example of Squeeze(aggr\_disti,j) dependence is shown in FIG. 6.

FIG. 6 is a graph illustrating a squeeze test measurement as a function of the distance between aggressor tracks in accordance with one embodiment of the invention. The vertical 20 axis represents squeeze in percent, and the horizontal axis represents the aggressor distance in uin. The vertical grid lines represent radial offsets that are accessible to the piezo actuator (integer multiples of the step size). As described above for one exemplary embodiment, the three aggressor 25 tracks on each side of the central track can result in nine values of aggressor distance. In the absence of any servo correction, the aggressor distances reported back by the piezo actuator are multiples of its finite step size (e.g., circular points 402 along lower line 404), as opposed to the more accurate values which are estimated by embodiments of the present algorithm (e.g., circular points 406 along upper line 408). The novel processes described herein can report a squeeze measurement value interpolated from the linear dependence at the desired aggressor distance (e.g., larger circular point 410) which is in stark discrepancy with the raw squeeze measurement value (e.g., diamond shaped point 412).

# FIG. 2—Block 218:

Returning now to FIG. 2, the process then determines a selected measurement using the estimated distance between the aggressor tracks, and the first and second track profiles of the central track in block 218. In several embodiments, the determination of the selected measurement involves interpo- 45 lation of measurement data.

In several embodiments, each of the preselected aggressor track offsets from the central track is different for each iteration of the process such that each is unique (e.g., organized to maximize the number of unique track separations).

In several embodiments, track profiles of the central track and/or aggressor tracks can be measured and stored. The track profiles can be thought of as a set of data reflecting the measuring and storing of an amplitude over a preselected radial offset range including the track of interest (e.g., central 55 track or aggressor track).

In some embodiments, the process is performed using a spin stand device such as the one described above and depicted in FIG. 1. In other embodiments, the process may be performed by a hard disk drive as part of a testing sequence. 60 In other embodiments, the process may be performed by other suitable disk testing equipment.

In several embodiments, the process is configured to determine the selected measurement as a squeeze measurement. In other embodiments, the selected measurement is a magnetic 65 track width measurement, a signal to noise ratio measurement, an error margin measurement, a shingled error margin

8

measurement, and/or another measurement that involves writing one or more aggressor tracks adjacent to a central track on a disk.

In several embodiments, the preselected measurement offset is a non-integer multiple of a minimum piezo-step size of a servo sub-component of a test instrument performing the writing the aggressor tracks on each side of the central track. In such case, the process can allow measurements to be made, often by way of interpolation, at locations that a spin stand or other disk testing machine cannot actually write directly to, or measure directly from.

In some embodiments, the process repeatedly writes the aggressor tracks on each side of the central track at the preselected aggressor track offset from the central track for a preselected number of iterations in block **208**. In one, embodiment, the preselected number of iterations is about 1 iterations (e.g., for a squeeze measurement). In another embodiment, the preselected number of iterations can be 3, 10, 100, 300, 1000, or 5000 iterations depending on the type of measurement value desired.

In one embodiment, the process 200 can perform the sequence of actions in a different order. In another embodiment, the process can skip one or more of the actions. In other embodiments, one or more of the actions are performed simultaneously. In some embodiments, additional actions can be performed.

In several embodiments, the process can seek to avoid duplicate data. For example, for the case when Npos is equal to Nneg which is also equal to N (see block **208** of the process where sectors are selected for aggressor tracks, and for example equal separations of offsets of sectors 0, 4, 8 in FIG. **4** where only one separation would represent particularly valuable data while the other two might be redundant) the list of aggressor distances in raw, uncorrected units contains duplicates:

- 3 times: 2\*<aggressor\_offset0>
- 2 times: 2\*<aggressor\_offset0>+N\*(piezo actuator step size)
- 2 times: 2\*<aggressor\_offset0>-N\*(piezo actuator step size)
- 1 time: 2\*<aggressor\_offset0>-2\*N\*(piezo actuator step size)
- 1 time: 2\*<aggressor\_offset0>+2\*N\*(piezo actuator step size)

To avoid this scenario, a non-symmetrical configuration (e.g., where Npos is not equal to Nneg) can be used in measurements on the spinstand. One additional benefit of the non-symmetrical configuration is that the range of aggressor distances becomes wider, and thus the linear fit Squeeze (aggr\_disti,j) is more reliable. An example of such data in shown in FIG. 7. The data shows that for one experimental spinstand tester, heads and media, any radial mis-positioning of the aggressor track writes of about plus or minus 0.04 uin (which is the radial step size of the hardware used in an experiment responsible for the data of FIG. 7) results in a squeeze error of about plus or minus 2%.

In several embodiments, the processes and test configurations described herein provide a number of advantages. For example, the processes and test configurations can improve the repeatability of spinstand measurements (e.g., squeeze measurements) over several servo writes by accurately determining where the aggressor tracks have been written. In one aspect, the processes and test configurations do not require additional hardware. In addition, the processes and test configurations can circumvent the hardware limitation imposed by the finite step size of the piezo actuator. The processes and

test configurations can also be extended to more complex measurements such as wsSNR and Error Rate/Error Margin with aggressor writes.

To assess some of the benefits of the proposed technique, several sets of measurements have been performed on a commercial Guzik spinstand tester. After writing a servo pattern. ten consecutive measurements were performed with the same testing conditions. A new servo was written and the procedure was repeated. In the end, a total of 100 measurements were performed, with ten new servo writes and ten measurements for each servo write. The goal of this procedure was to assess the repeatability of the measurements. An example of such data which were acquired is shown in FIG. 7.

FIG. 7 is a graph illustrating squeeze test measurement data acquired on a commercial spinstand in a series of repeated measurements with ten servo writes (vertical dashed lines) in accordance with one embodiment of the invention. The vertical axis represents squeeze in percent, and the horizontal axis represents the measurement number. As can be seen in 20 FIG. 7, the data acquired with the servo correction algorithm 502 significantly reduces the scatter in the raw data 506. In addition, the algorithm presented in this application 504 provides the best repeatability of the data. The new algorithm 504 reduces not only the overall data scatter (servo write to servo 25 write) but also the data scatter within a servo write.

The raw data 506, which were measured without enabling the servo correction algorithm, exhibit a significant scatter due to the systematic and non-systematic errors that affect the radial positioning system. The implementation of the servo 30 correction algorithm decreases the data scatter by reducing the systematic errors. However, it appears that the servo correction algorithm cannot reduce the non-systematic errors. The range of squeeze values is larger than about 4%, which is associated with about plus or minus 0.04 uIn (corresponding 35 to about plus or minus 1 nanometer or nm) radial positioning error. The proposed algorithm reduces even further the systematic errors due to the inaccuracies of the servo pattern, as shown in FIG. 7. The range of Squeeze values reported by the proposed algorithm is below about 2%, which is associated 40 with about plus or minus 0.02 uIn (plus or minus 0.5 nm) radial positioning error. Squeeze ranges smaller than 2% have been consistently observed in a number of experiments, also at other radii than a midpoint.

In one aspect, a penalty of the proposed algorithm can be 45 the test time. In some embodiments, for example, the aggressors are written at more than one location on each side of the central track, and the aggressor tracks are profiled, such that the testing time is higher compared to a conventional squeeze algorithm. However, the use of particular sectors can reduce 50 the testing time.

It shall be appreciated by those skilled in the art in view of the present disclosure that although various exemplary fabrication methods are discussed herein with reference to magnetic recording disks, the methods, with or without some 55 modifications, may be used for fabricating other types of recording disks, for example, optical recording disks such as a compact disc (CD) and a digital-versatile-disk (DVD), or magneto-optical recording disks, or ferroelectric data storage devices.

While the above description contains many specific embodiments of the invention, these should not be construed as limitations on the scope of the invention, but rather as examples of specific embodiments thereof. Accordingly, the scope of the invention should be determined not by the 65 embodiments illustrated, but by the appended claims and their equivalents.

10

For example, in several embodiments, the methods described herein can be used to improve disk media characterization of magnetic disks used in hard disk drives. However, in other embodiments, the methods described herein can be used to improve spinstand testing of other devices under

The various features and processes described above may be used independently of one another, or may be combined in various ways. All possible combinations and sub-combinations are intended to fall within the scope of this disclosure. In addition, certain method, event, state or process blocks may be omitted in some implementations. The methods and processes described herein are also not limited to any particular sequence, and the blocks or states relating thereto can be performed in other sequences that are appropriate. For example, described tasks or events may be performed in an order other than that specifically disclosed, or multiple may be combined in a single block or state. The example tasks or events may be performed in serial, in parallel, or in some other suitable manner. Tasks or events may be added to or removed from the disclosed example embodiments. The example systems and components described herein may be configured differently than described. For example, elements may be added to, removed from, or rearranged compared to the disclosed example embodiments.

What is claimed is:

- 1. A method for improving measurements involving aggressor tracks, the method comprising:
  - (a) erasing a circumferential band of a disk;
  - (b) writing a central track on the circumferential band of the disk;
  - (c) measuring and storing a first track profile of the central track;
  - (d) writing an aggressor track on each side of the central track at a preselected aggressor track offset from the central track;
  - (e) measuring and storing a track profile of the aggressor tracks at the preselected aggressor track offset from the central track;
  - (f) measuring and storing a second track profile of the central track;
  - performing (a) through (f) exactly n times where n is greater than or equal to 1;
  - determining an estimated distance between the aggressor tracks based on the respective track profiles; and
  - determining a selected measurement using the estimated distance between the aggressor tracks, and the first and second track profiles of the central track.
  - 2. The method of claim 1:

wherein n is greater than or equal to 2;

- wherein for each iteration of (a) through (f) of the method, the preselected aggressor track offset from the central track is unique; and
- wherein the determining the selected measurement using the estimated distance between the aggressor tracks, and the first and second track profiles of the central track comprises determining the selected measurement at a preselected offset from the central track using interpolation, the estimated distance between the aggressor tracks for each iteration, and the first and second track profiles of the central track.
- 3. The method of claim 2:

60

wherein the (d) writing the aggressor track on each side of the central track at the preselected aggressor track offset from the central track comprises writing the aggressor track on each side of the central track at the preselected

- aggressor track offset from the central track in preselected sectors of the disk; and
- wherein the (e) measuring and storing the track profile of the aggressor tracks comprises measuring and storing the track profile of the aggressor write tracks at the preselected aggressor track offset from the central track and the preselected sectors of the disk.
- **4.** The method of claim **3**, further comprising selecting the preselected aggressor track offsets and the preselected sectors such that for a preselected group of the preselected sectors, each combination of the preselected aggressor track offsets for a particular sector are unique.
- 5. The method of claim 4, wherein n is equal to or greater than 3 such that at least 3 iterations of (a) through (f) are  $_{15}$  performed.
- **6.** The method of claim **5**, wherein the preselected group of the preselected sectors is 9 sectors.
- 7. The method of claim 6, wherein the writing the aggressor track on each side of the central track at the preselected aggressor track offset from the central track in preselected sectors of the disk comprises:
  - (g) writing 1 sector for every 3 consecutive sectors on a first side of the central track;
  - (h) writing 3 consecutive sectors for every 9 consecutive 25 sectors on a second side of the central track; and
  - performing (g) and (h) exactly 3 times at 3 different preselected aggressor track offsets.
- 8. The method of claim 1, wherein for each iteration of (a) through (f) of the method, the preselected aggressor track 30 offset from the central track is unique.
- 9. The method of claim 1, wherein n is equal to or greater than 3 such that at least 3 iterations of (a) through (f) are performed.
  - 10. The method of claim 1:
  - wherein the measuring and storing the first track profile of the central track comprises measuring and storing an amplitude over a preselected radial offset range comprising the central track; and
  - wherein measuring and storing the track profile of the 40 aggressor tracks at the preselected aggressor track offset from the central track comprises measuring and storing an amplitude over a preselected radial offset range comprising a respective one of the aggressor tracks.
  - 11. The method of claim 1:
  - wherein the (e) measuring and storing the track profile of the aggressor tracks at the preselected aggressor track offset from the central track comprises:
    - performing a servo calibration procedure and thereby generating a read offset correction table; and
    - applying the read offset correction table to the track profiles of the aggressor tracks thereby generating corrected track profiles of the aggressor tracks, and
  - wherein the determining the estimated distance between the aggressor tracks based on the respective track profiles comprises determining the estimated distance between the aggressor tracks based on the respective corrected track profiles.
- 12. The method of claim 11, wherein the performing the servo calibration procedure and thereby generating the read 60 offset correction table comprises:
  - writing a series of tracks over a range of read offsets to be calibrated:
  - measuring a set of raw track profiles from the series of tracks:
  - sampling the set of raw track profiles at a series of signal amplitude levels;

12

- constructing a reference track profile from the set of sampled track profiles;
- calculating a set of read offset deltas from each sampled track profile; and
- merging the sets of read offset deltas into a set of average read offset deltas stored in the read offset correction table.
- 13. The method of claim 1, wherein the method is performed using a test instrument selected from the group consisting of a spin stand and a hard disk drive.
- 14. The method of claim 1, wherein the selected measurement is selected from the group consisting of a squeeze measurement, a magnetic track width measurement, a signal to noise ratio measurement, an error margin measurement, a shingled error margin measurement, and combinations thereof.
- 15. The method of claim 1, wherein the preselected measurement offset comprises a non-integer multiple of a minimum piezo-step size of a servo of a test instrument performing the writing the aggressor track on each side of the central track.
- 16. The method of claim 1, wherein the writing the aggressor track on each side of the central track at the preselected aggressor track offset from the central track comprises repeatedly writing the aggressor tracks on each side of the central track at the preselected aggressor track offset from the central track for a preselected number of iterations.
- 17. A system for improving measurements involving aggressor tracks, the system comprising:
  - a test platform configured to receive, support, and rotate a disk configured for magnetic information storage;
  - a magnetic transducer configured to write to, and read from, the disk;
  - a memory;

35

50

- a processor coupled to the memory, the magnetic transducer, and the test platform, the processor configured to:
  (a) erase a circumferential band of a disk;
  - (b) write a central track on the circumferential band of the disk;
  - (c) measure and store a first track profile of the central track:
  - (d) write an aggressor track on each side of the central track at a preselected aggressor track offset from the central track;
  - (e) measure and store a track profile of the aggressor tracks at the preselected aggressor track offset from the central track;
  - (f) measure and store a second track profile of the central track;
  - perform (a) through (f) exactly n times where n is greater than or equal to 1;
  - determine an estimated distance between the aggressor tracks based on the respective track profiles; and
  - determine a selected measurement using the estimated distance between the aggressor tracks, and the first and second track profiles of the central track.
- 18. The system of claim 17:
- wherein n is greater than or equal to 2;
- wherein for each iteration of (a) through (f), the preselected aggressor track offset from the central track is unique; and
- wherein the processor is further configured to determine the selected measurement at a preselected offset from the central track using interpolation, the estimated distance between the aggressor tracks for each iteration, and the first and second track profiles of the central track.

19. The system of claim 18:

wherein the processor is configured to write the aggressor track on each side of the central track at the preselected aggressor track offset from the central track in preselected sectors of the disk; and

wherein the processor is configured to measure and store the track profile of the aggressor tracks at the preselected aggressor track offset from the central track at the preselected sectors of the disk.

- 20. The system of claim 19, wherein the processor is further configured to select the preselected aggressor track offsets and the preselected sectors such that for a preselected group of the preselected sectors, each combination of the preselected aggressor track offsets for a particular sector are unique.
- 21. The system of claim 20, wherein n is equal to or greater than 3 such that at least 3 iterations of (a) through (f) are performed.
- **22.** The system of claim **21**, wherein the preselected group of the preselected sectors is 9 sectors.
- 23. The system of claim 22, wherein the processor is configured to write the aggressor track on each side of the central track at the preselected aggressor track offset from the central track in the preselected sectors of the disk by:
  - (g) writing 1 sector for every 3 consecutive sectors on a first side of the central track;
  - (h) writing 3 consecutive sectors for every 9 consecutive sectors on a second side of the central track; and
  - performing (g) and (h) exactly 3 times at 3 different preselected aggressor track offsets.
- **24**. The system of claim **17**, wherein for each iteration of (a) through (f), the preselected aggressor track offset from the central track is unique.
- **25**. The system of claim **17**, wherein n is equal to or greater than 3 such that at least 3 iterations of (a) through (f) are performed.
  - 26. The system of claim 17:
  - wherein the processor is configured to measure and store the first track profile by measuring and storing an amplitude over a preselected radial offset range comprising the central track;
  - wherein the processor is configured to measure and store the track profile of the aggressor tracks by measuring

14

and storing an amplitude over a preselected radial offset range comprising a respective one of the aggressor tracks.

27. The system of claim 17:

wherein the processor is further configured to:

perform a servo calibration procedure and thereby generating a read offset correction table;

apply the read offset correction table to the track profiles of the aggressor tracks thereby generating corrected track profiles of the aggressor tracks; and

determine the estimated distance between the aggressor tracks based on the respective corrected track profiles.

28. The system of claim 27, wherein the processor is further configured to:

write a series of tracks over a range of read offsets to be calibrated:

measure a set of raw track profiles from the series of tracks; sample the set of raw track profiles at a series of signal amplitude levels;

construct a reference track profile from the set of sampled track profiles;

calculate a set of read offset deltas from each sampled track profile; and

merge the sets of read offset deltas into a set of average read offset deltas stored in the read offset correction table.

- **29**. The system of claim **17**, wherein the system comprises a test instrument selected from the group consisting of a spin stand and a hard disk drive.
- 30. The system of claim 17, wherein the selected measurement is selected from the group consisting of a squeeze measurement, a magnetic track width measurement, a signal to noise ratio measurement, an error margin measurement, a shingled error margin measurement and combinations thereof.
- 31. The system of claim 17, wherein the preselected measurement offset comprises a non-integer multiple of a minimum piezo-step size of a servo of a test instrument performing the writing the aggressor track on each side of the central track.
- **32**. The system of claim **17**, wherein the processor is configured to repeatedly write the aggressor tracks on each side of the central track at the preselected aggressor track offset from the central track for a preselected number of iterations.

\* \* \* \* \*